## Asymmetrically Biased Balanced Power Amplifier

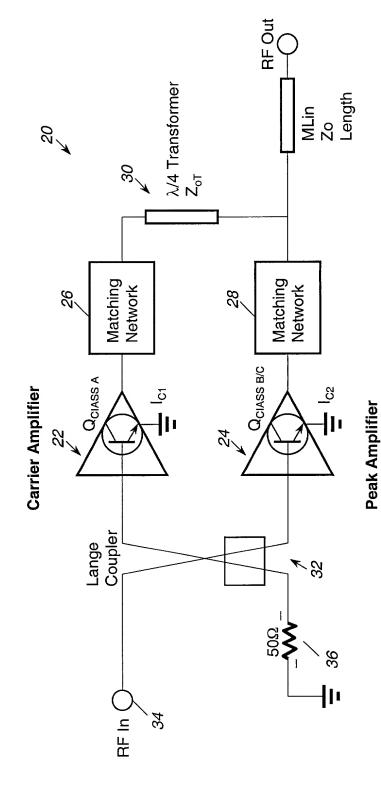
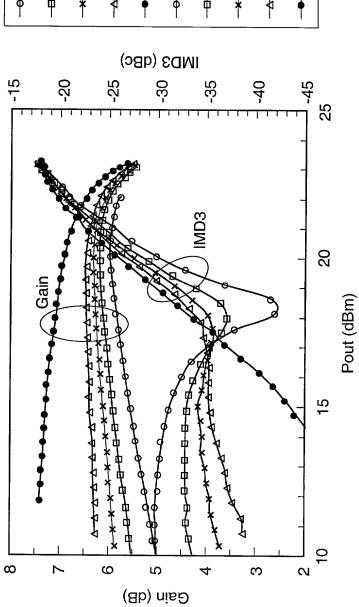


FIG. 1



—<del>в</del>— IMD3[Ic1=60mA; Ic2=2.4mA] --- IMD3[lc1=60mA; lc2=0.3mA] -0- Gain[Ic1=60mA; Ic2=0.3mA] —— Gain[Ic1=60mA; Ic2=2.4mA] --- IMD3[Ic1=64mA; Ic2=64mA] --- IMD3[Ic1=60mA; Ic2=10mA] -A- Gain[lc1=60mA; lc2=10mA] --- Gain[lc1=64mA; lc2=64mA] -\*- IMD3[Ic1=60mA; Ic2=5mA] -x- Gain[lc1=60mA; lc2=5mA]

FIG. 2

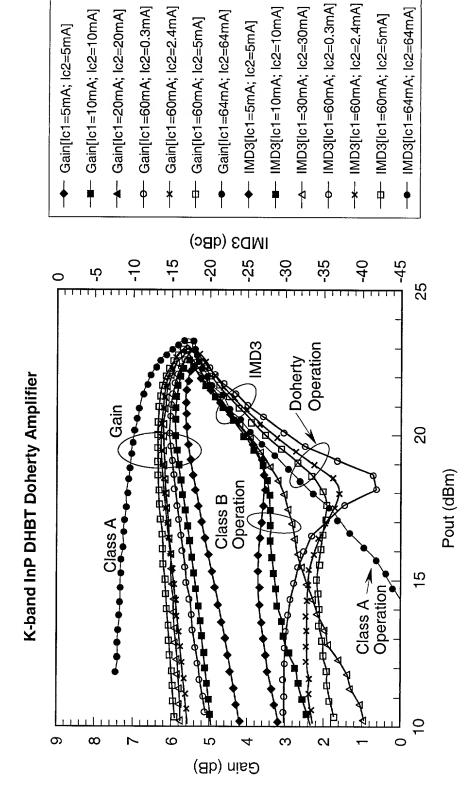
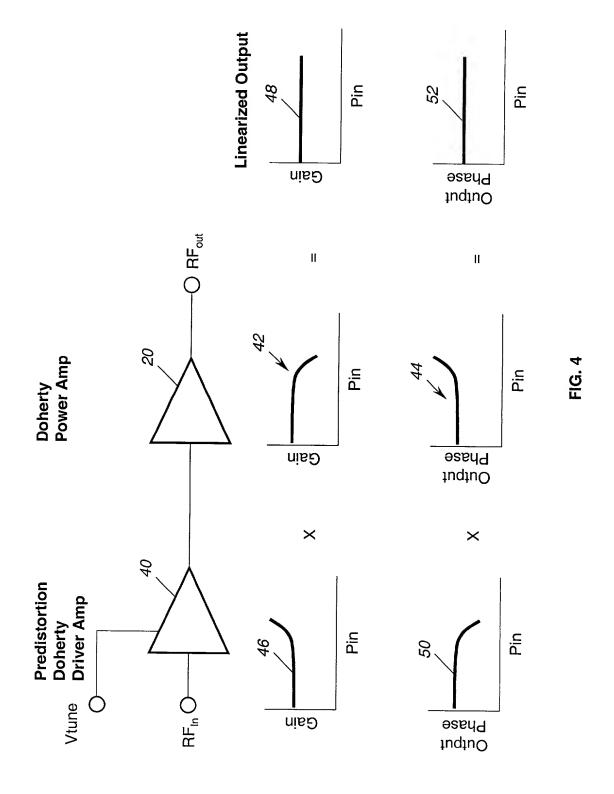


FIG. (



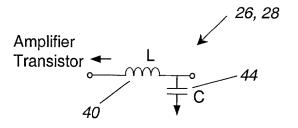


FIG. 5A

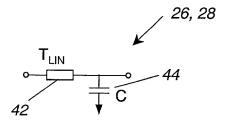


FIG. 5B

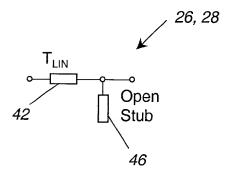


FIG. 5C

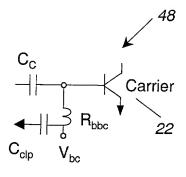


FIG. 6A

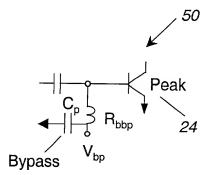


FIG. 6B